Rishin Rahim

Senior Machine Learning Engineer

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Work Experience

Senior Machine Learning Engineer, BlueOptima - July 2021 to Present

- Currently leads the development of machine learning models capable of identifying developer signatures in code and detecting AI generated code, assessing the impact of AI on developer productivity and quality.
- Developed models that discern developer behaviour patterns by analysing the version control data and commit patterns.
- Successfully implemented models capable of automatically mapping identified dependency vulnerabilities to published vulnerabilities in NVD. Utilised text data from diverse sources such as JIRA Bug reports, Github issues, release notes, and security advisories.
- Manage and maintain end-to-end deployment of ML models in production environments. The development/production infrastructure is set up on an on-premise cloud integrated with CUDA GPUs using kubernetes, mlflow and docker.

Senior Machine Learning Engineer, Unisys India - Jan 2020 to June 2021

- Contributed to the development of IntelliServe[™], a conversational AI tool where we built models to identify the intent of a conversation and built utterance models to generate conversations using knowledge graphs to create synthetic training data.
- Contributed significantly to the development of CloudForte[™] where I developed time series forecasting models, leading to significant resource optimization in cloud environments. Also developed recommendation models aiding users in making informed decisions about upscaling or downscaling of resources.
- The development environment included Databricks and Pyspark. The ML models were deployed in Azure cloud.

Machine Learning Engineer, Tata Consultancy Services - Mar 2015 to Jan 2020

- Key contributor to the development of Contract Digitisation[™], an AI platform for Legal Documents. Designed and implemented a robust pipeline for collecting, storing, cleaning, and transforming legal documents. Developed models using various NLP techniques for extracting standard agreement clauses, and actively participated in model deployment and monitoring.
- Instrumental in the development of SmartQE[™], a test suite optimization model that significantly
 improved test coverage and reduced redundancy. Created a Defect Prediction model
 forecasting the number of defects in future application releases. Additionally, developed a
 similarity check model to measure the degree of similarity between different test step
 executions.

Education

• Master of Science (MS), Information Technology, 2012 - 2014 Indian Institute of Information Tech and Management Kerala

Master's Thesis: Threshold logic Object Detection using FPGAs .Thesis Advisor : Dr Alex P James. Two novel techniques for object detection are presented, based on Resistive threshold logic and binary XNOR operation. The design and verification is done using Verilog HDL The design is then synthesised and mapped into FPGA.

• Bachelor of Technology (B.Tech.), Information Technology, 2007 - 2011 Cochin University of Science and Technology

Certifications

TensorFlow in Practice Specialization - Coursera (DEQUZEVE97A5)

Skills

Machine Learning, Deep Learning, Data analysis, Natural Language Processing(NLP), Large Language Models (LLM), MLOps, Python, Pytorch, tensorflow, pyspark, pandas, mlflow, Kubernetes, Docker.